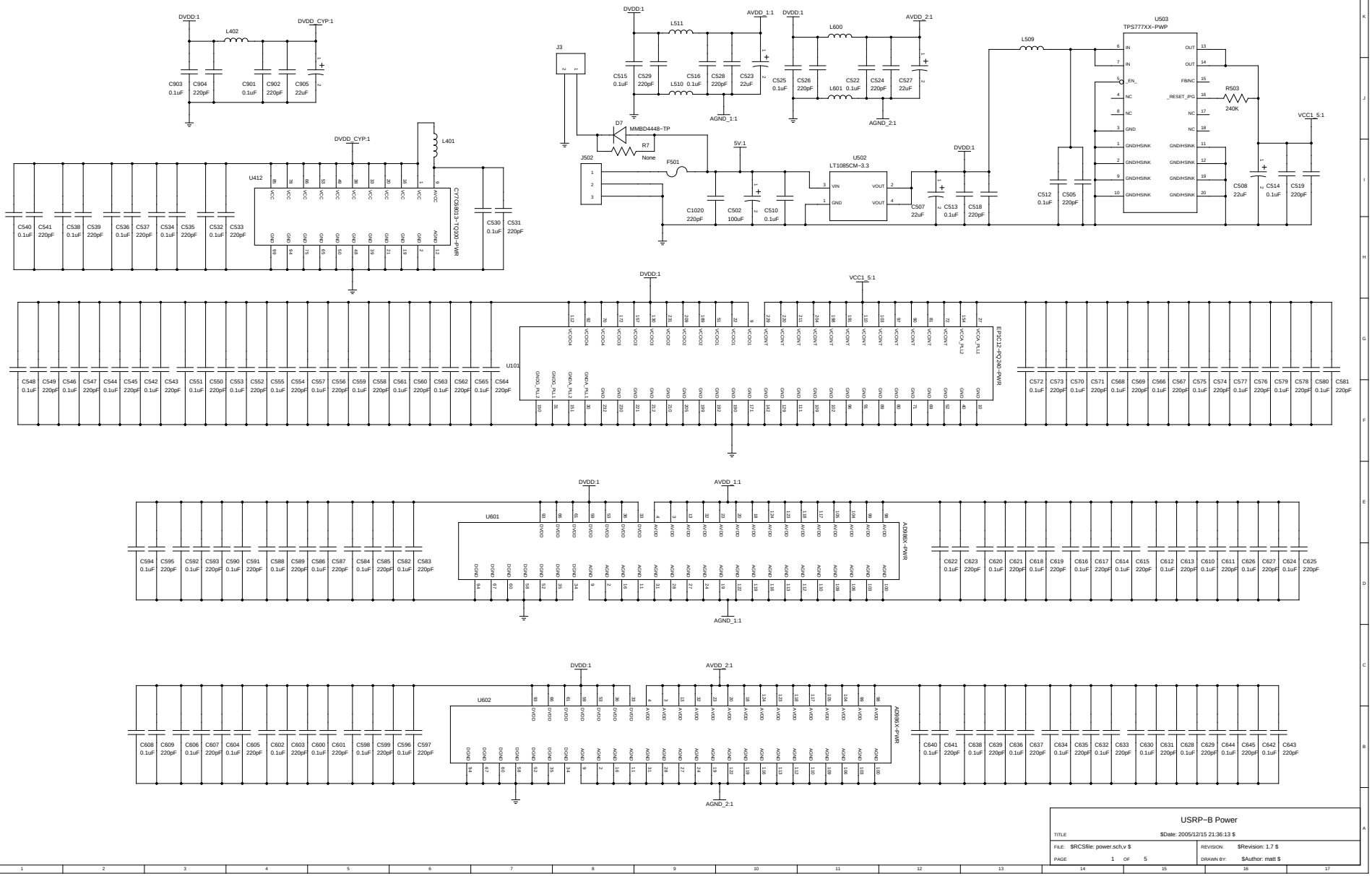
DVDD:1
DVDD_CYP:1
L402
C903 0.1uF
C904 220pF
C901 0.1uF
C902 220pF
C905 22uF
L401
U412
CY7C68013-TQ100-PWR
C540 0.1uF
C541 220pF
C538 0.1uF
C539 220pF
C536 0.1uF
C537 220pF
C534 0.1uF
C535 220pF
C532 0.1uF
C533 220pF
C530 0.1uF
C531 220pF
J3
L511
AVDD_1:1
C515 0.1uF
C529 220pF
L510 0.1uF
C516
C528 220pF
C523 22uF
AGND_1:1
L600
AVDD_2:1
C525 0.1uF
C526 220pF
L601 0.1uF
C522
C524 220pF
C527 22uF
AGND_2:1
D7
MMBD4448-TP
R7
None
J502
F501
5V:1
C1020 220pF
C502 100uF
C510 0.1uF
U502
LT1085CM-3.3
VIN
VOUT
GND
C507 22uF
C513 0.1uF
C518 220pF
L509
U503
TPS777XX-PWP
IN
OUT
EN_
NC
GND
GND/HSINK
FB/NC
RESET/PG
R503
240K
C512 0.1uF
C505 220pF
C508 22uF
C514 0.1uF
C519 220pF
VCC1_5:1
U101
EP1C12-PQ240-PWR
C548 0.1uF
C549 220pF
C546 0.1uF
C547 220pF
C544 0.1uF
C545 220pF
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C543 220pF
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C578 220pF
C580 0.1uF
C581 220pF
U601
AD9867-PWR
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AD9867-PWR
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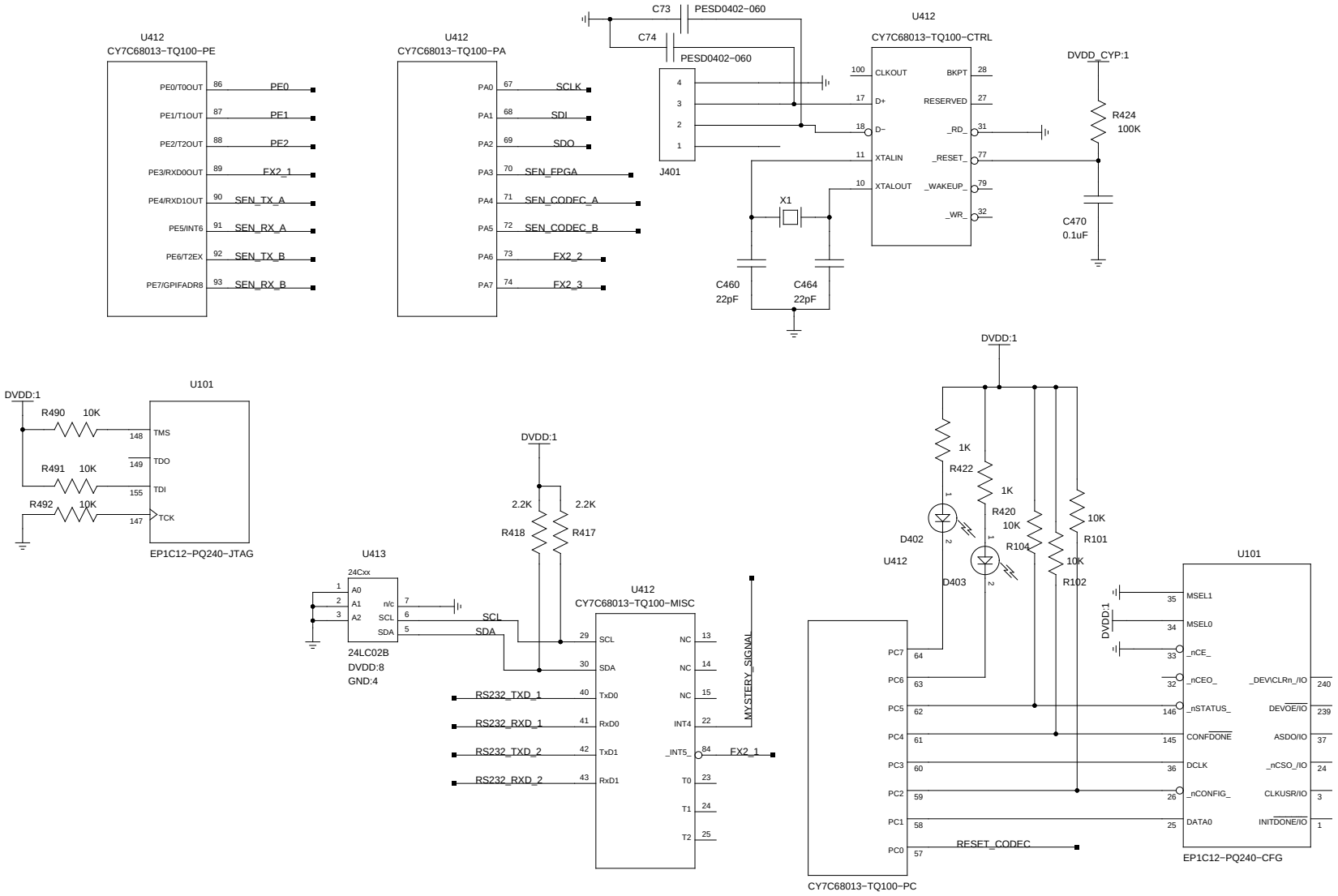
USRP−B Power
TITLE
$Date: 2005/12/15 21:36:13 $
FILE: $RCSfile: power.sch,v $
REVISION: $Revision: 1.7 $
PAGE 1 OF 5
DRAWN BY: $Author: matt $



USRP-B FPGA/FX2 Interface			
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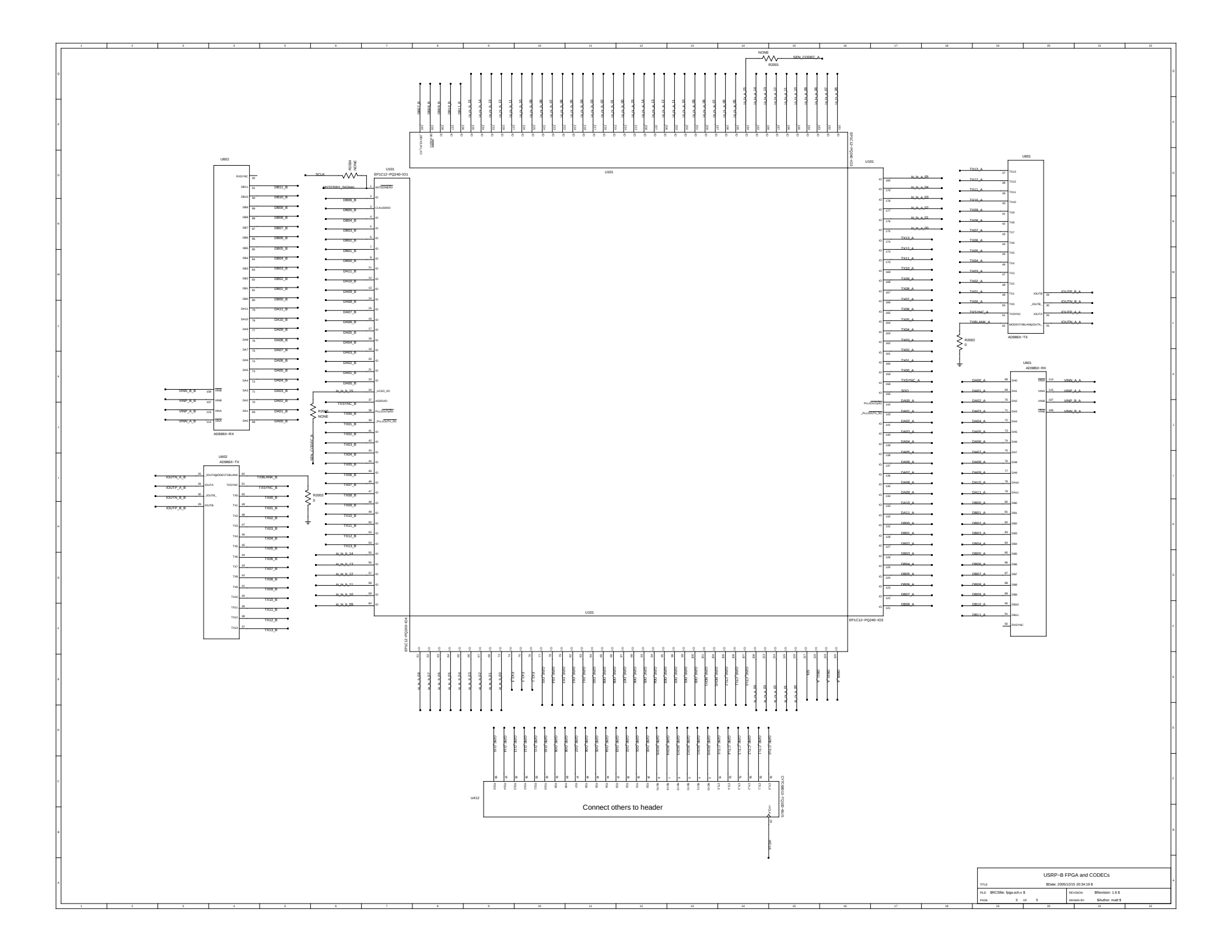
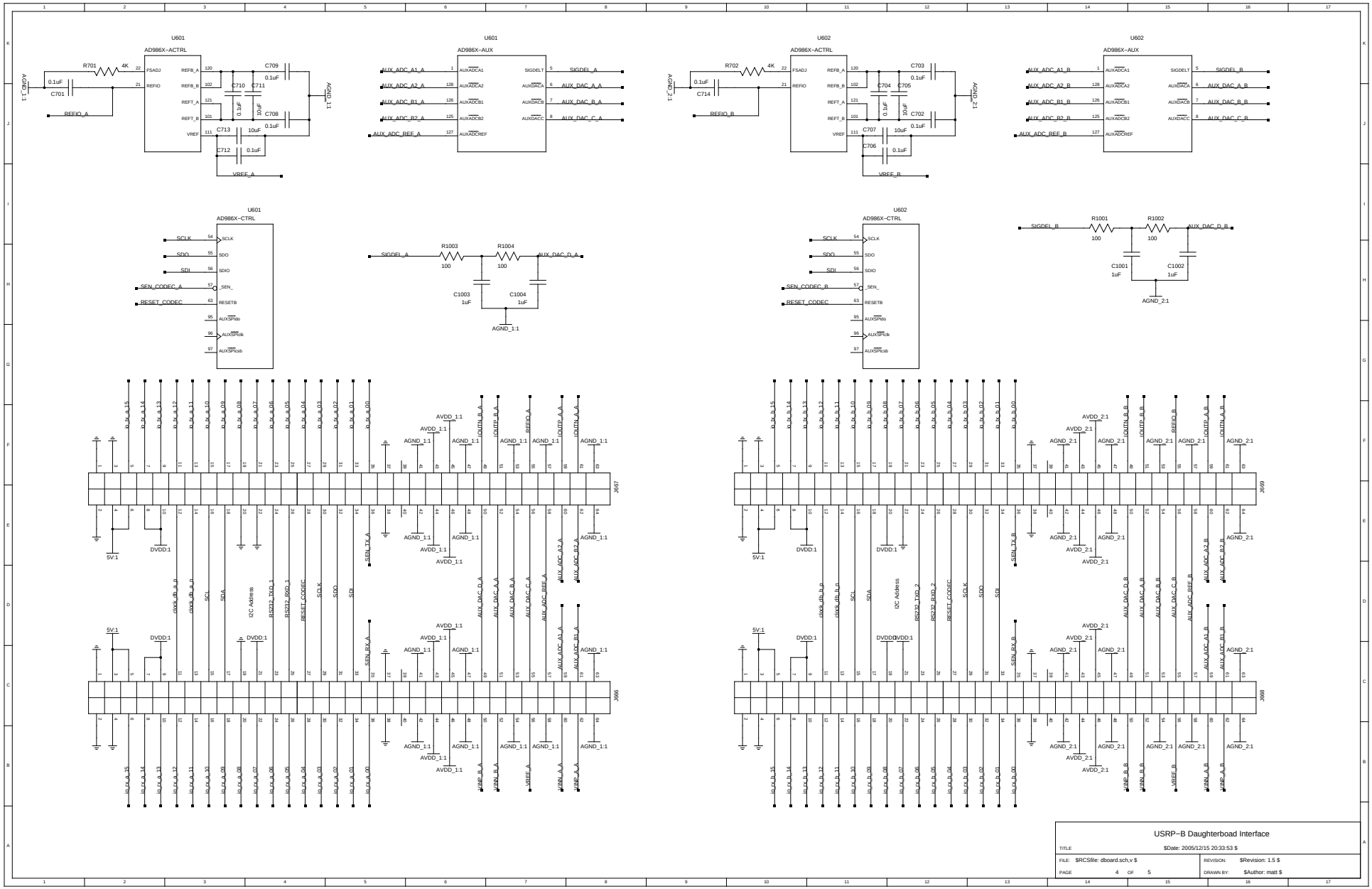
PAGE 2 OF 5		DRAWN BY:	$Author: matt $


U602
AD98BX-RX
U602
AD98BX-TX
U101
EP1C12-PQ240-IO1
U101
EP1C12-PQ240-IO2
U101
EP1C12-PQ240-IO3
U101
EP1C12-PQ240-IO4
U601
AD98BX-TX
U601
AD98BX-RX
U412
Connect others to header
R2001
NONE
SEN_CODEC_A
R2002
0
R2003
0
R2004
NONE
SCLK
MYSTERY_SIGNAL
SEN_CODEC_B
USRP-B FPGA and CODECs
$Date: 2005/12/15 20:34:19 $
$RCSfile: fpga.sch,v $
$Revision: 1.6 $
$Author: matt $



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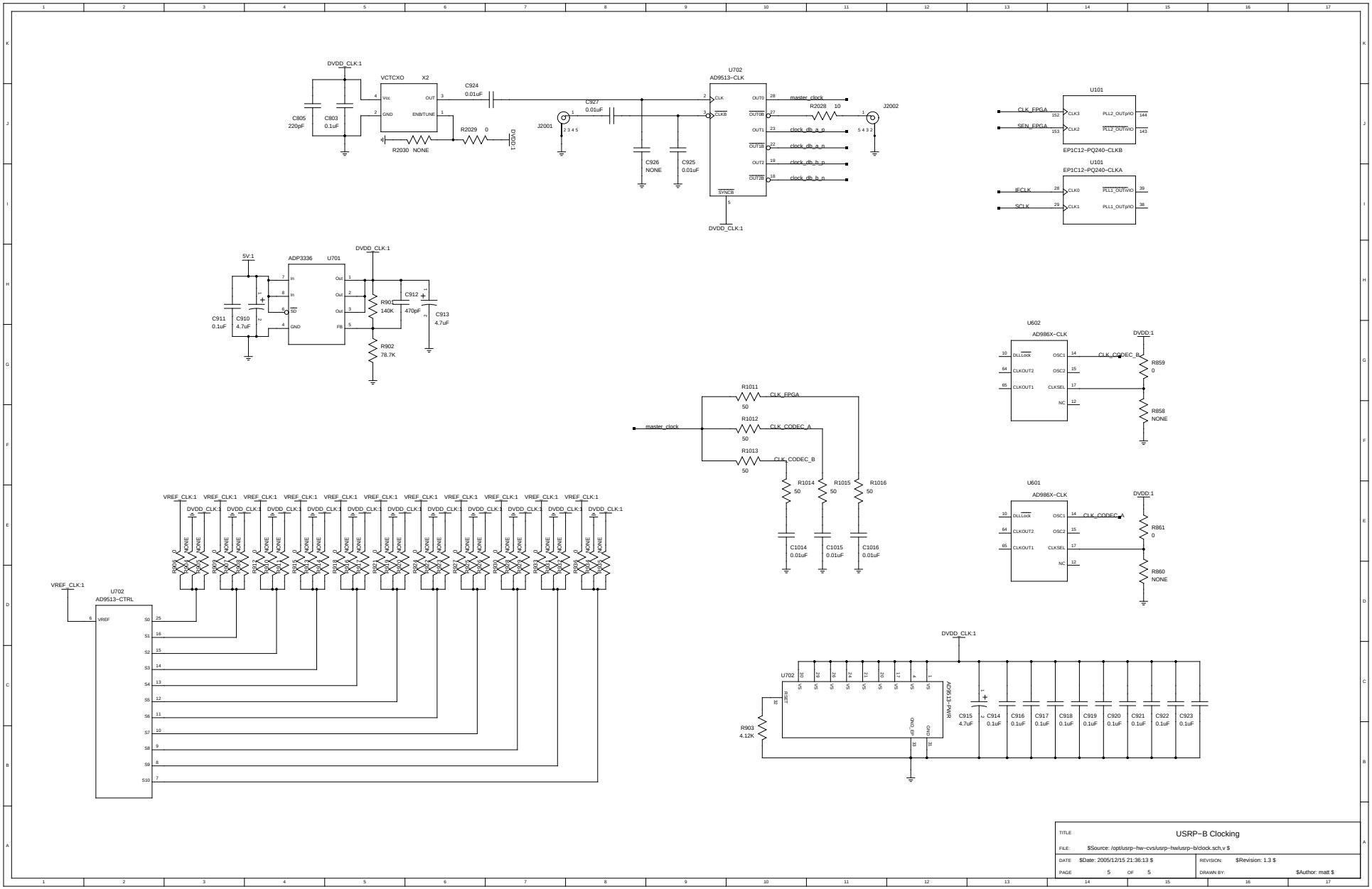
PAGE 4 OF 5	DRAWN BY: $Author: matt $



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DATE	$Date: 2005/12/15 21:36:13 $
REVISION:	$Revision: 1.3 $
PAGE	5 OF 5
DRAWN BY:	$Author: matt $